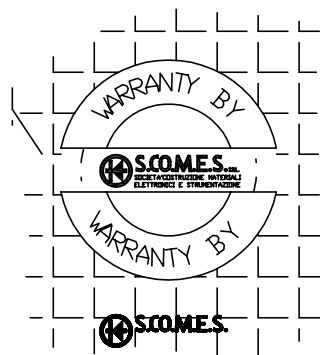
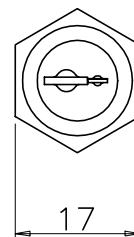
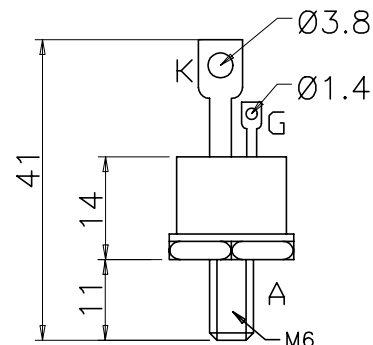
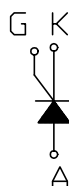


## Features

- ⊗ High current rating
- ⊗ For general purpose application
- ⊗ Superior surge capabilities
- ⊗ Standard package
- ⊗ Metric thread version available

## Ratings and Characteristics

| Parameters              | 50RIA      | Units            |
|-------------------------|------------|------------------|
| $I_{T(AV)}$             | 50         | A                |
| $I_{T(AV)}$ @ $T_C$ MAX | 94         | A                |
| $I_{T(RMS)}$            | 80         | A                |
| $I_{TSM}$ @ 50Hz        | 1200       | A                |
| $I_{TSM}$ @ 60Hz        | 1255       | A                |
| $I^2t$ @ 50Hz           | 7200       | A <sup>2</sup> s |
| $I^2t$ @ 60Hz           | 6560       | A <sup>2</sup> s |
| $I_{GT}$                | 100        | mA               |
| $V_{RRM}$               | 50 to 1200 | V                |
| $t_q$ typical           | 110        | μs               |
| $T_J$                   | -40 to 125 | °C               |



## Voltage Ratings

| Type number | Voltage Code | $V_{RRM}/V_{DRM}$<br>max. repetitive peak reverse or off-state voltage<br>V (2) | $V_{RSM}$<br>maximum non-repetitive peak reverse voltage<br>V (1) | $I_{DM}$ $I_{RM}$ max.<br>@ 125 °C<br>mA |
|-------------|--------------|---|---|--|
| 50RIA       | 40           | 400   | 500   | 15                                       |
|             | 80           | 800   | 900   |  |
|             | 120          | 1200  | 1300  |  |

## ELECTRICAL SPECIFICATIONS

### On-state Conduction

| Parameter   | 50RIA   | Units         | Conditions  |  |
|---|---------|---------------|---|--|
| $I_{T(AV)}$ Max. average on-state current                   | 50      | A             | 180° sinusoidal conduction  |  |
| $I_{T(RMS)}$ Max. RMS on-state current                      | 80      | A             | Following any rated load condition, and with rated $V_{RRM}$ applied following surge SCR turnec fully on. |  |
| $I_{TSM}$ Max. peak, one-cycle non-repetitive surge current | 1200    |               |   | 50Hz half cycle sine wave or 6ms rectangular pulse |
|   | 1255    |               |   | 60Hz half cycle sine wave or 5ms rectangular pulse |
|   | 1430    |               |   | 50Hz half cycle sine wave or 6ms rectangular pulse |
| $I^2t$ Maximum $I^2t$ for fusing                            | 7200    | $A^2s$        | Rated $V_{RRM}$ applied following surge initial $T_J = 125^\circ C$                                       |  |
|   | 6560    |               |   | $t = 10ms$   |
|   | 10180   |               | $V_{RRM}$ following surge = 0, initial $T = 125^\circ C$  | $t = 10ms$   |
|   | 9300    |               |   | $t = 8.3ms$  |
| $I^2\sqrt{t}$ Maximum $I^2\sqrt{t}$ for dev. fusing (3)     | 101.800 | $A^2\sqrt{s}$ | $t = 0.1$ to $10ms$ $V_{RRM}$ following surge = 0, initial $T = 125^\circ C$                              |  |
| $V_{TM}$ Max peak on-state voltage                          | 1.6     | V             | $T_J = 25^\circ C$ , $I_{T(AV)} = 50A$ (157A peak)  |  |
| $I_H$ Max holding current                                   | 1.14    | mA            | $T_J = 25^\circ C$ , anode supply = 22V, initial $I_T = 2A$   |  |
| $I_L$ Max latching current                                  | 400     | mA            | Anode supply = 6V, resistive load   |  |

### Blocking

| Parameter  | 50RIA | Units      | Conditions   |
|--|-------|------------|--|
| $dv/dt$ Min. critical rate of rise of of state voltage | 200   | V/ $\mu s$ | $T_J = 125^\circ C$ , exponential to 100% rated $V_{DRM}$ Zero gate bias voltage gate open circuited |
|  | 500   | V/ $\mu s$ | $T_J = 125^\circ C$ , exponential to 100% rated $V_{DRM}$ Zero gate bias voltage gate open circuited |

### Switching

| Parameter   | 50RIA                    | Units   | Conditions  |
|---|--------------------------|---|---|
| $di/dt$ Max. non repetitive rate of rise of turned-on current<br>$V_{RRM} = 50$ to $600V$<br>$V_{RRM} = 50$ to $600V$ | 200                      | A/ $\mu s$  | $T_C = 25^\circ C$ $V_{DM} = \text{rated } V_{DRM}$ , $I_{TM} = 10a$ dc resistive circuit, Gate pulse: 10V, 15ohm, $t_p = 6\mu s$ , $t_r = 0.1\mu s$ max. |
|   |                          |   |   |
|   | $t_d$ Typical delay time |   |   |
| $t_q$ Typical turn-off time   | 110                      | $I_{TM} = 50A$ , $T_J = 125^\circ C$ , $di/dt = -10A/\mu s$ , $V_R = 50V$<br>$dv/dt = 20V/\mu s$ , Gate 0V 100 $\Omega$ |   |

### ⊗ Triggering

| Parameter                                    | 50RIA | Units | Conditions   |
|--|-------|-------|--|
| $P_{GM}$ Maximum peak gate power             | 10.0  | W     | $t_p \ll 5ms$  |
| $P_{G(AV)}$ Maximum average gate power       | 2.5   | W     |  |
| $I_{GM}$ Max. peak positive gate current     | 2.5   | A     |  |
| $+V_{GM}$ Maximum peak positive gate voltage | 20    | V     |  |
| $-V_{GM}$ Maximum peak negative gate voltage | 10    |       |  |
| $I_{GT}$ DC gate current required to trigger | 250   | mA    | $T_J = -40^\circ C$<br>$T_J = 25^\circ C$<br>$T_J = 125^\circ C$ |
|  | 100   |       |  |
|  | 50    |       |  |
| $V_{GT}$ DC gate current required to trigger | 3.5   | V     | $T_J = -40^\circ C$<br>$T_J = 25^\circ C$                        |
|  | 2.5   |       |  |
| $I_{GD}$ DC gate current not to trigger      | 5.0   | mA    | $T_J = 125^\circ C$  |
| $V_{GD}$ DC gate voltage not to trigger      | 0.2   | V     |  |

Max. required gate trigger/current/voltage are the lowest value which will trigger all units 6V anode-to-cathode applied

Max. gate current/voltage not to trigger is the max. value which will not trigger any unit with rated  $V_{DRM}$  anode-to-cathode applied

### ⊗ Thermal and Mechanical Specification

| Parameter   | SCT180        | Units    | Conditions                               |
|---|---------------|----------|--|
| $T_J$ Max.operating temperature range               | -40 to 125    | °C       |  |
| $T_{stg}$ Max.storage temperature range             | -40 to 125    |          |  |
| $R_{th-C}$ Max.thermal resistance, junction to case | 0.35          | deg.C/W  | DC                                       |
| $R_{thCS}$ Max.thermal resistance, case to sink     | 0.25          | deg.C/W  | Mounting surface smooth,flat and greased |
| T Mounting torque                                   | min. 2.8 (25) | N/m      | Non lubricated threads                   |
|   | max. 3.4 (30) | (lbf/in) |  |
| wt Approximate weight                               | min. 28       | gr.      |  |

(1) For voltage pulses with  $t_p \ll 5ms$

(2) Units may be broken over non repetitively in the off state direction without damage,if  $di/dt$  does not exceed  $20A/\mu s$

(3)  $I^2 t$  for time  $t_x = I^2 \sqrt{t} \cdot \sqrt{t_x}$